Status of Claims

Claims 1-20 cancelled.

21. (new) A computer system comprising:

a power supply for providing a voltage;

at least two boards, each board receiving the voltage, and wherein each board comprises

at least one voltage regulator, for receiving the voltage and for providing a regulated voltage level to the board, and

at least one processor for controlling the regulated voltage level,

wherein the processor monitors a value of at least one power-related parameter on the board and controls the voltage regulator in such a way as to influence a subsequent value of the at least one parameter.

22. (new) The computer system of claim 21 wherein the processor, upon detection of a fault associated with the at least one power related parameter, shuts down the board.

23. (new) The computer system of claim 21 wherein the at least one power-related parameter is a regulated voltage of the board.

24. (new) The computer system of claim 21 wherein the at least one power-related parameter is a temperature value of the board.

25. (new) The computer system of claim 21 wherein each board further comprises a signaling interface for receiving instructions therefrom, and wherein, the processor is responsive to the received instructions for controlling the at least one voltage regulator.

26. (new) The computer system of claim 25 wherein the processor causes data to be written to a system log file, wherein the data is associated with the at least one power-related parameter.

27. (new) The computer system of claim 21 further comprising an interface for coupling to a console for receiving instructions therefrom for controlling various ones of the processors on each of the at least two boards.

28. (new) The computer system of claim 23 wherein the processor collects temperature values over time for performing a time-based analysis of the collected temperature values.

29. (new) A computer system comprising:

a plurality of boards, each board comprising a power control element, wherein the power control element comprises a regulator for providing a regulated voltage to the board and a processor for monitoring and controlling the regulator; and

a signaling interface coupled to each power control element of each of the plurality of boards for communicating data to, and from, each one of the processors,

wherein the processor for each board monitors a value of at least one power-related parameter for its board and controls its regulator in such a way as to influence a subsequent value of the at least one parameter.

30. (new) The computer system of claim 29 wherein the processor for each board, upon detection of a fault associated with the at least one power related parameter, shuts down its board.

31. (new) The computer system of claim 29 wherein the at least one power-related parameter is a regulated voltage of the board.

32. (new) The computer system of claim 29 wherein the at least one power-related parameter is a temperature value of the board.

33. (new) The computer system of claim 29 wherein the processor for each board is responsive to instructions received from the signaling interface for controlling its regulator.

34. (new) The computer system of claim 29 wherein the processor for each board causes data to be written to a system log file via the signaling interface and wherein the data is associated with the at least one power-related parameter of its board.

35. (new) The computer system of claim 29 further comprising an interface for coupling to a console for receiving instructions therefrom for controlling various ones of the processors on each board.

36. (new) The computer system of claim 29 further comprising a central controller coupled to the signaling interface for controlling the processor on each of the plurality of boards.

37. (new) The computer system of claim 36 wherein the central controller causes data to be written to a log file representative of information received, via the signaling interface, with respect to at least one power-related parameter of one of the plurality of boards.

38. (new) The computer system of claim 36 further comprising an interface for coupling the central controller to a console for receiving instructions therefrom for controlling various ones of the processors on each board.